LAB 7:

19L-1316

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| --- | --- |
| org 0h  main:  jnb p1.4,$  call debounce  mov a,p1  anl a,#0fh  mov r1,a  jnb p1.4,$  call debounce  mov a,p1  mov a,p1  anl a,#0fh  mov r2,a  jb p0.1,ml  jnb p0.1,ad  bk:  mov p2,a  call delay2  mov p2,#0ffH  jmp main  debounce:  call delay  jb p1.4,$  call delay  ret  delay2: | mov r3,#50h  lp1: mov r4,#255h  lp2: mov r5,#255h  lp3: djnz r5,lp3  djnz r4,lp2  djnz r3,lp1  ret  delay:  mov r3,#2h  lp4: mov r4,#243h  lp5: mov r5,#255h  lp6: djnz r5,lp6  djnz r4,lp5  djnz r3,lp4  ret  ad:  mov a,r1  add a,r2  jmp bk  ml:  mov a,r1  mov b,r2  mul ab  jmp bk  end |

PROTEUS CIRCUIT OUTPUT:

